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United states patent [19] 4,874,963

United States Patent [19] Alspector [54] NEUROMORPHIC LEARNING NETWORKS [75] Inventor: Joshua Alspector, West?eld, N.J. Bell Communications Research, Inc.,

Patent us4874963 - neuromorphic learning networks

A neuron network which achieves learning by means of a modified is provided for each neuron for the purpose of simulated annealing of the network.

Analog vlsi stochastic perturbative learning

We present analog VLSI neuromorphic architectures for Analog VLSI Implementation of Neural Systems Analog VLSI Stochastic Perturbative Learning

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Circuits for VLSI implementation of temporally asymmetric Hebbian learning. Documents; Authors; Adaptive WTA With an Analog VLSI Neuromorphic Learning Chip

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A VLSI recurrent network of integrate-and-fire Theory, simulation, VLSI implementation and learning in VLSI recurrent neural networks.

Vlsi implementation of a bio-inspired olfactory

VLSI Implementation of a neuromorphic spiking neural and are normalized before training and testing to ensure that the classification result is only caused by

Patent us5479579 - cascaded vlsi neural network

are cascaded for larger sizes and enhanced resolution. A hardware-compatible algorithm permits hardware-in-the-loop learning despite limited weight resolution.

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